

Indira Gandhi Institute of Technology, Sarang, Dhenkanal-759146
Department of Electrical Engineering
NOTICE

No.IGIT/Elect/ 318

Date: 18.9.2024

The following students of 5th semester BTech in Electrical Engineering, have not appeared in the lab test and viva-voce of Electrical Machines Laboratory – II (PCEE7203) at regular time and as well as the default notice (IGIT/Elect/310) for the second time. They are hereby advised to attend the lab test and viva on dated 19.09.2024 (section A) at 9:40am & 20.09.2024 (Section B) at 8:00am otherwise they may be treated as absent. No further extensions of dates regarding lab test and viva.

Sl no	Regd No	Name	Defaulter Details
2 1	2201105114	BISWANATH MAHANTA	Lab Test & Viva voce
4 2	2201105124	DEEPANSHU BEHERA	Lab Test & Viva voce
8 3	2201105213	SUBHASANDIPAN BEHERA	Lab Test & Viva voce
8 4	2201105207	SRI JITENDRA KUMAR SAHOO	Lab Test & Viva voce
7 5	2201105142	LALIT MOHAN PRUSTY	Viva voce
8 6	2201105160	PRAYAS RAJ SAHU	Viva voce

Rohini
18.9.2024

Head of the Department
Department of Electrical Engineering
IGIT, Sarang

Copy to:

1. Students' Notice Board, Dept. of Electrical Engineering.
2. Hostel Notice Board (Bhaskar Bhawan, Brahmos Bhawan)
3. prof. in charge website for notification in website.